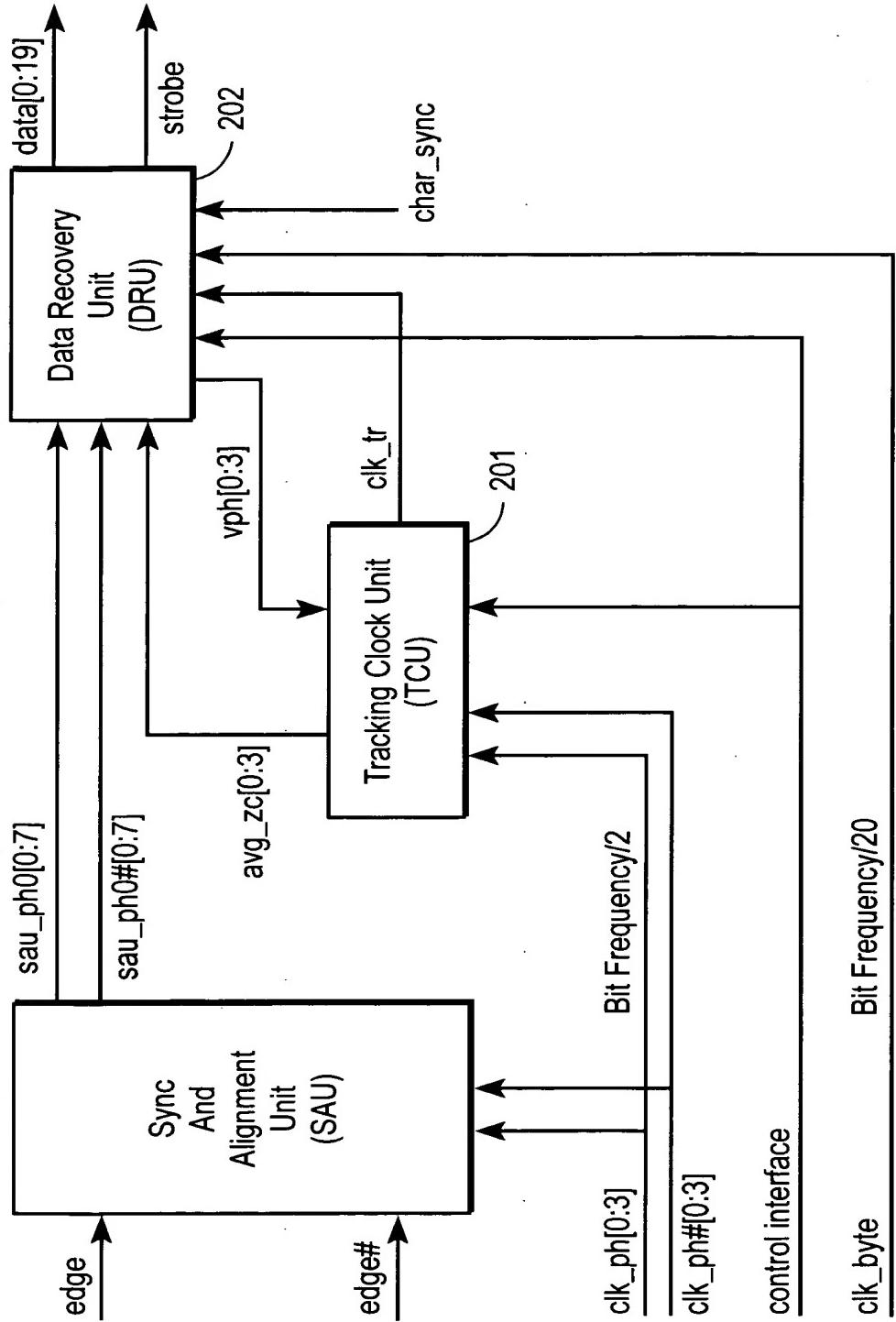


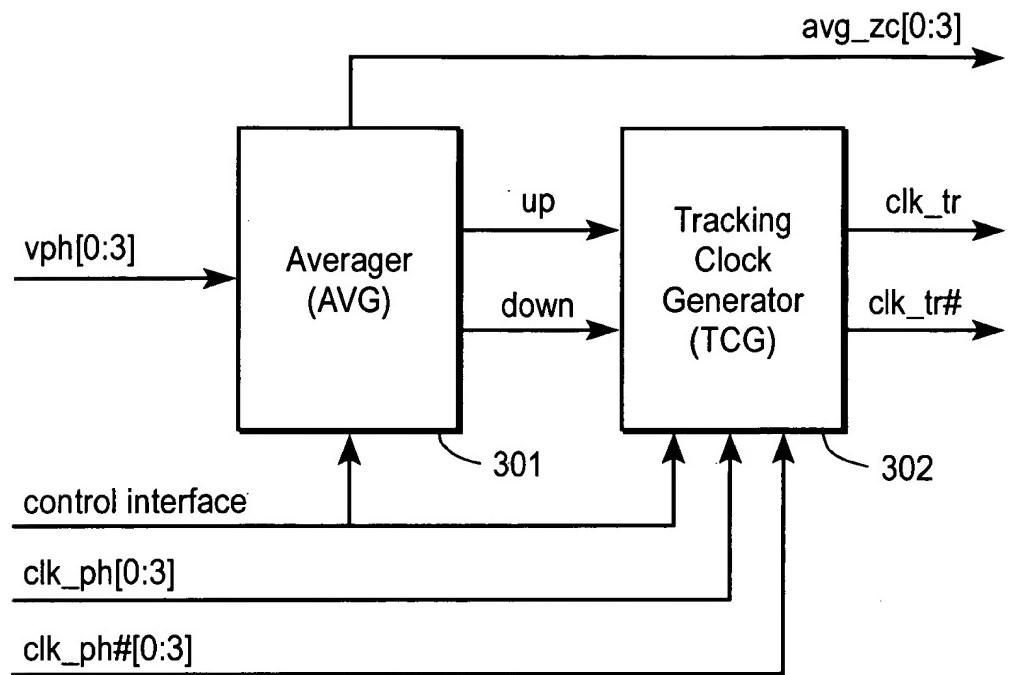
High Level Block Diagram of Edge Based Receiver for 2.5Gb/s Operation

FIG. 1



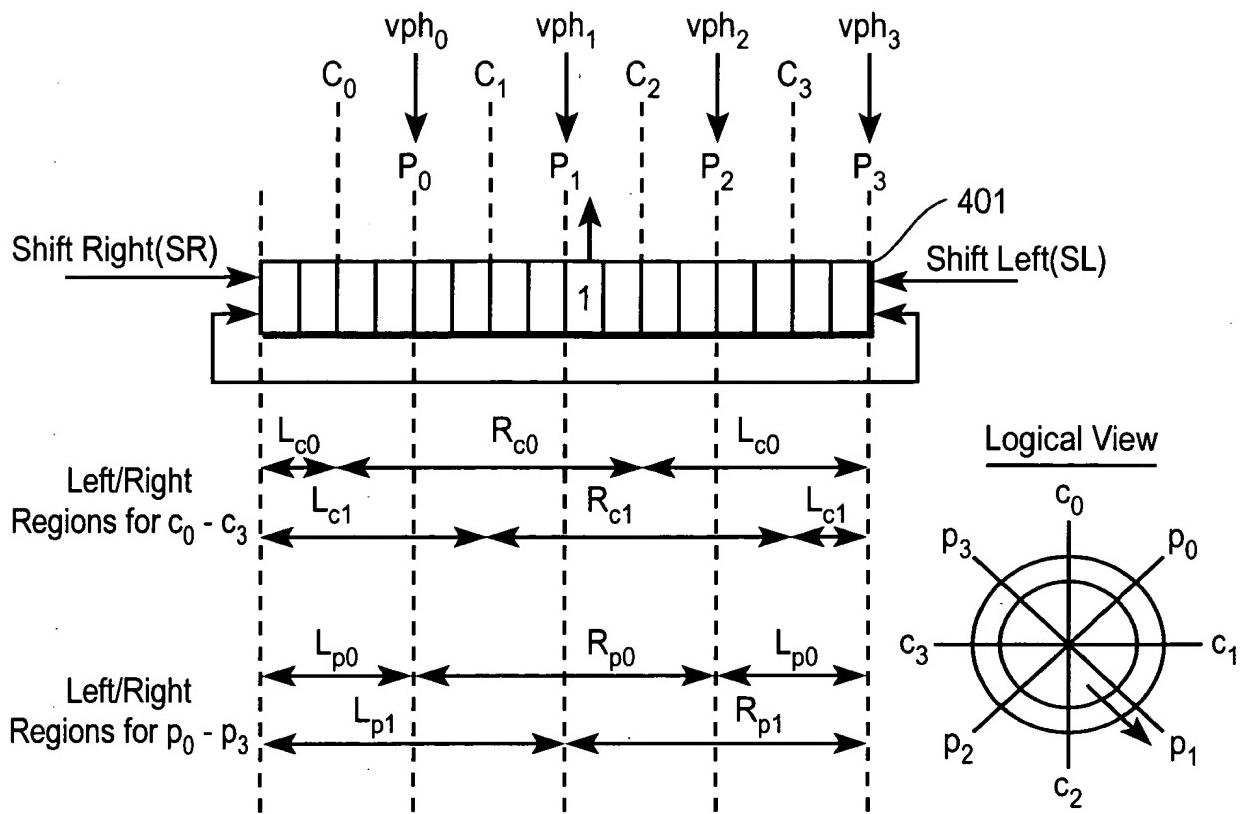
Edge Processing Block Diagram

FIG. 2



Tracking Clock Unit (TCU) Block Diagram

FIG. 3



Barrel Shifter for Phase Tracking

FIG. 4

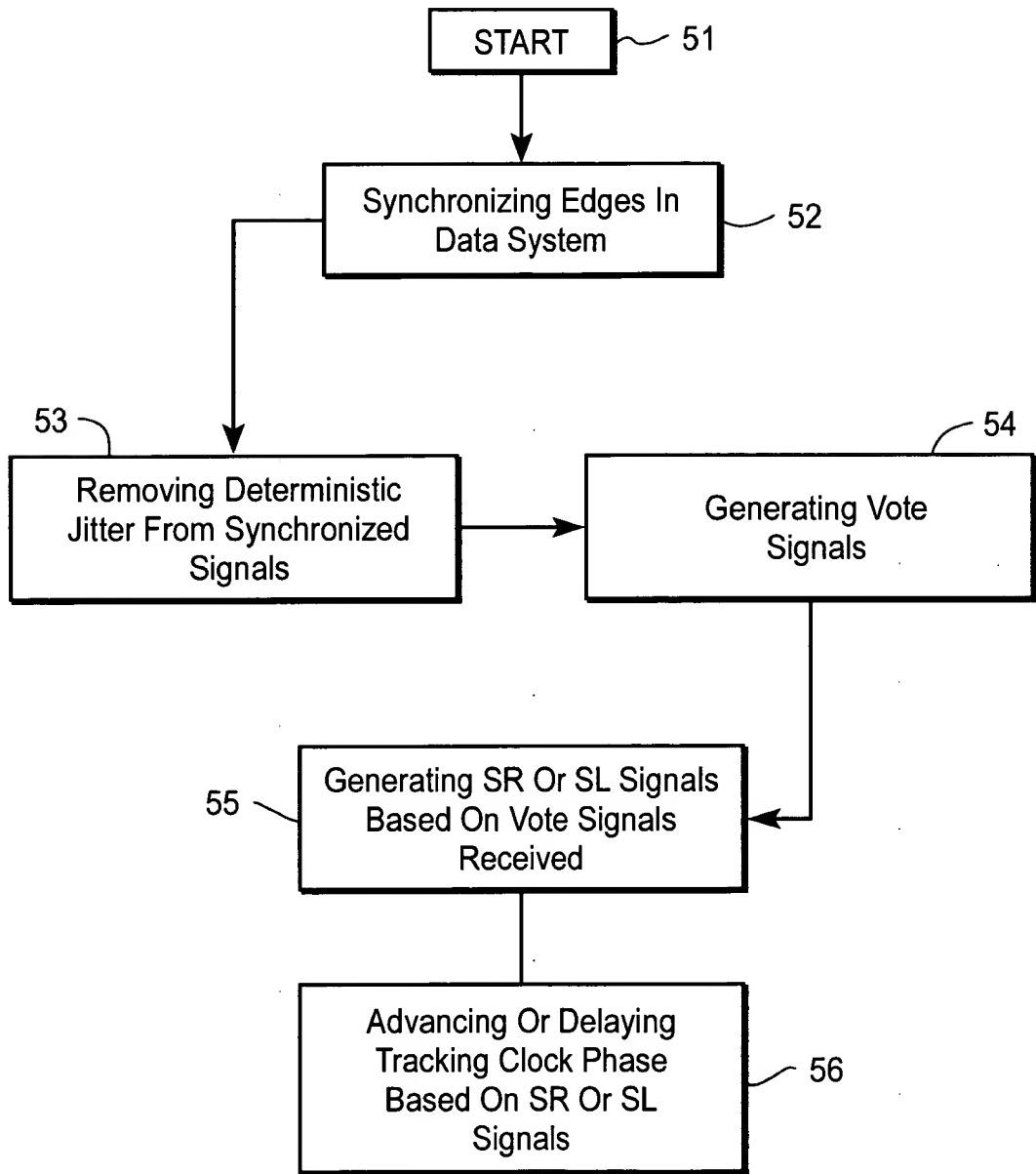


FIG. 5

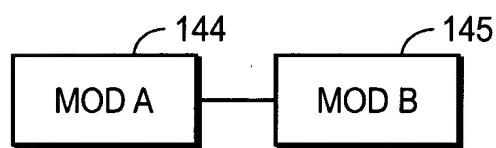
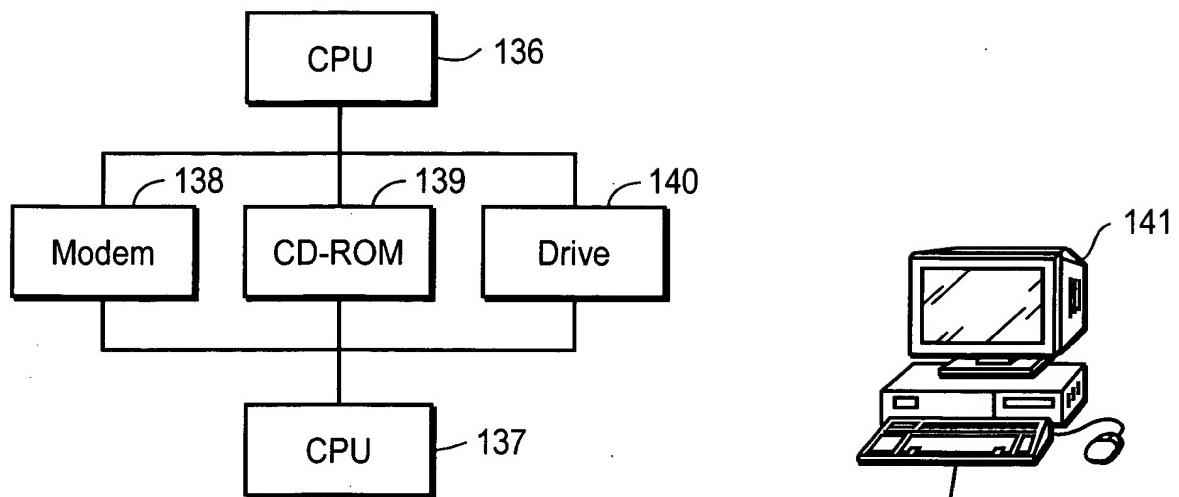


FIG. 6c

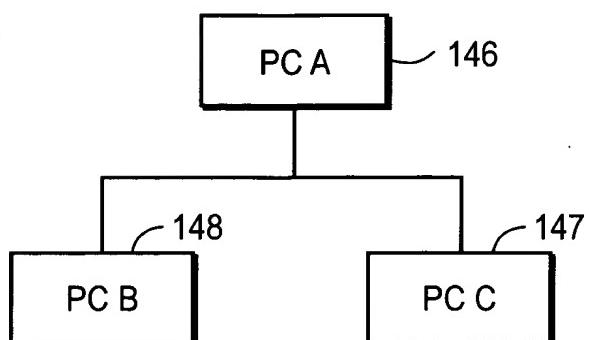


FIG. 6d

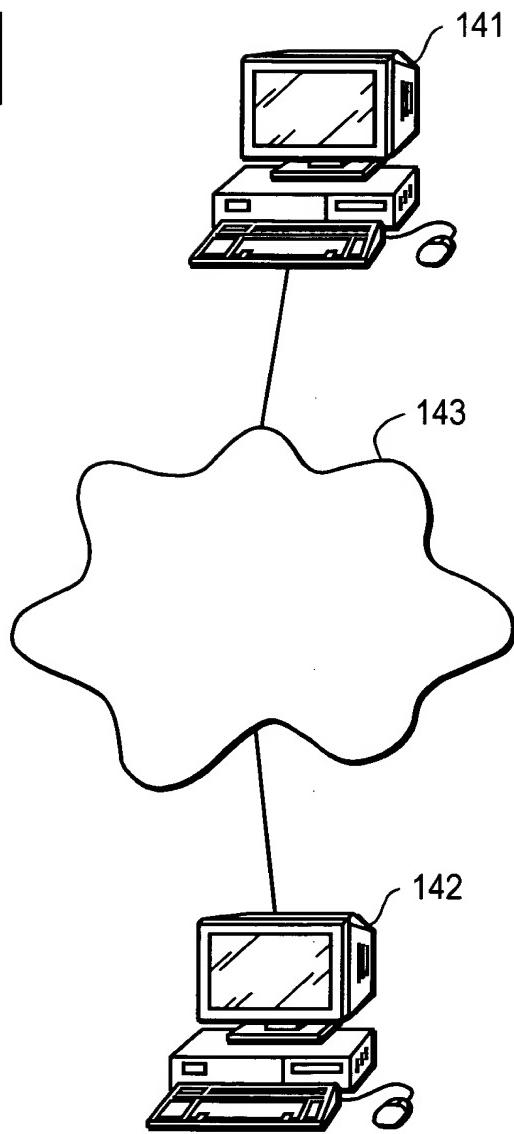


FIG. 6b